**AMENDMENT TRANSMITTAL LETTER**Attorney Docket
No: **CD03011**Application Serial Number:
10/799,742Filing Date:
03/12/2004Examiner:
Mai, Son LuuGroup Art Unit:
2827**Invention: Method and Circuit for Reducing Defect Current From Array Element Failures in Random Access Memories****TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:**

Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	19	MINUS	20	0	\$50	\$ 0.00
INDEP. CLAIMS	3	MINUS	3	0	\$200	\$ 0.00

Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of _____ to and through _____, comprising an extension of the shortened statutory period of:

_____ one month (\$60) _____ three months (\$510)
_____ two months (\$225) _____ four months (\$795)**TOTAL ADDITIONAL FEE FOR THIS AMENDMENT**_____ A check in the amount of **\$ 0.00** is attached.☒ Please charge any necessary additional fees or credit any overpayment to Deposit Account **50-2087**.☒ No additional fee is required.**Conditional Petition for Additional Extension of Time**

It is believed that the indicated items are timely filed. However, in the event an additional extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a).

Please charge any fees that may be required to Deposit Account No. **50-2087**.August 4, 2005
Date
Bradley T. Sako, Reg. No. 37,92337 C.F.R. §1.8

I hereby certify that this correspondence is being

☐ transmitted via facsimile to the United States Patent and Trademark Office to fax number: _____
Date of Transmittal: _____☒ deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: August 4, 2005

Typed/Printed Name: Bradley T. Sako

Signature: 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

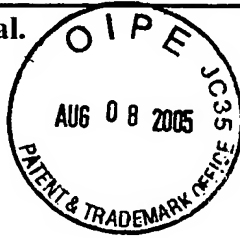
Applicant(s): PARENT et al.

Serial No.: 10/799,742

Filed: March 12, 2004

Title: Method and Circuit for Reducing
Defect Current From Array Element
Failures in Random Access Memories

Attorney Docket No.: CD03011



Group Art Unit: 2827

Examiner: Mai, Son Luu

RESPONSE TO OFFICE ACTION

5 Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

10 A. Introductory Comments

The following is submitted in response to the Office Action dated May 10, 2005, and is
currently due August 10, 2005.

25 37 C.F.R. §1.8

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30 ☐ transmitted via facsimile to the United States Patent and Trademark Office to fax number: _____

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P.O. Box 1450, Alexandria, VA 22313-1450.

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Typed/Printed Name: Bradley T. Sako

40 Signature: 